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Dossis, M.F.; Noras, J.M.; Porter, G.J.;

Circuits and Systems, 1994. ISCAS '94., 1994 IEEE International Symposium on, Volume: 1, 30 May-2 June 1994

Pages:229 - 232 vol.1

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Klaus Hofmann, Manfred Glesner, Nicu Sebe, A. Manolescu, Santiago Marco, Josep Samitier, Jean-Michel Karam, Bernard Courtois

 March 1997 **Proceedings of the 1997 European conference on Design and Test**

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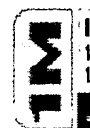

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A CAD tool for the automated generation of behavioral models in HDL-A is presented. This CAD tool has been implemented in the frame of a project for the automatic modeling of microsystem components for the co-simulation with VHDL or Spice-models. Starting from the finite-element-description of a microcomponent a nonlinear behavioral HDL-A-model is generated by successively adding or deleting effects to the HDL-A-model according to the observed differences between the two models. Using the exampl ...

Keywords: logic CAD, HDL-A-model, micromembrane, finite-element-description, CAD tool, automated generation, behavioral models, FEM, microsystem components, automatic modeling, cosimulation, VHDL-models, Spice-models



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1 Use of statecharts in the modelling of dynamic behaviour in the ATL DAO prototype-1

Croll, P.; Duval, P.-Y.; Jones, R.; Kolos, S.; Sari, R.F.; Wheeler, S.;
Nuclear Science, IEEE Transactions on , Volume: 45 , Issue: 4 , Aug. 1998
Pages:1983 - 1988

[\[Abstract\]](#) [\[PDF Full-Text \(716 KB\)\]](#) **IEEE JNL**

2 A retargetable optimizing code generator for digital signal processors

Kreuzer, W.; Gotschlich, M.; Wess, B.;
Circuits and Systems, 1996. ISCAS '96., 'Connecting the World'. 1996 IEEE
International Symposium on , Volume: 2 , 12-15 May 1996
Pages:257 - 260 vol.2

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Pages: 297 - 318

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